On page 8, lines 3-17, change the paragraphs to read as follows:

For example, where the first T-carrier 18 is a T1 carrier, odd frames may be written into a first twenty-four successive memory locations of memory space. Even frames may be written into the next twenty-four successive memory locations in memory space to provide a total memory requirement of forty-eight locations for the first T-carrier. Further, the odd and even memory spaces may be interleaved.

Similarly, the second T-carrier 20 may also be a T1 carrier, which writes odd frames into twenty-four successive memory locations of another location of the memory space and even frames into twenty-four successive memory locations in the memory space. As with the first T-carrier, the odd and even memory spaces may be interleaved.

On page 10, lines 3-9, change the paragraph to read as follows:

To facilitate the cross-connect process, data may be exchanged between memory 30 and the third T-carrier 22 sequentially. For example, data may be exchanged on a first channel, then a second channel, a third, and so on. As with the first and second T-carriers, the third T-carrier may use a modulo forty-eight counter to keep track of channel number (and address).